

B1 UNIVERSAL SERIAL BIT STREAM PROCESSOR

ABSTRACT OF THE DISCLOSURE

A serial bit stream processor implemented as a bit-addressable memory. The memory (1001) accommodates four input serial bit streams at input ports A, B, C, and D. A 5 16-bit opcode input at memory input (1003) determines the binary output at the single bit output (1005). The opcode uniquely addresses each of sixteen memory locations to selectively output one of sixteen possible combinations based upon the combination of input variables.